DYNANIC

# Powering next-generation networks with flexible, high-performance SmartNICs

Investor Deck — March 2026

# Leaders Driving Innovation: Founders with Deep Tech & Scale Expertise

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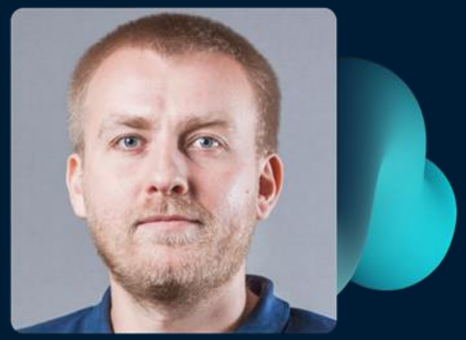

## Pavol Korcek (PhD)

### Co-Founder & CEO

— **10 years experience as a managing director**
— Co-founder of RehiveTech spin-off
— Successful **exit to the CZ holding**
— Online **marketing expertise for international products**

## Lukas Kekely (PhD)

### Co-Founder & CTO

— **15 years of experience as a researcher** & FPGA domain expert
— 10 years in **management and leading of R&D** with international teams
— 25+ research **papers in the area**

## Jan Korenek (PhD)

### Co-Founder & CSO

— 17 years **experience as a serial founder of tech. companies**
— Founded Netcope Technologies and Flowmon Networks, which was acquired by Kemp in 2020
— **Sold IP to Intel** in 2020

To strengthen the sustainability of quality / key employees, we will introduce an **Employee Stock Option Plan (ESOP)**

# Networking Under Pressure – Rise of SmartNICs

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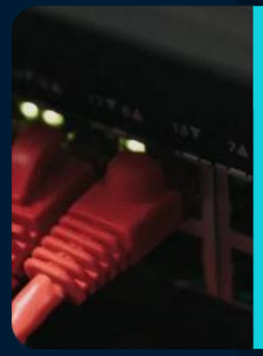

## AI revolution drives data growth

Increasing link speeds to 400G and beyond

## 400G

400G SmartNICs will dominate by 2028

## CPU performance limits scalability

Too many CPU cores wasted on networking

## Perform

Most servers use SmartNICs by 2028

## Fixed hardware limits adaptability

Evolving cyber threats, standards and services

## Flexible

Increasing SmartNIC value over time

* Data comes from Dell'Oro analysis

## Fast growing market of SmartNICs

up to 30% annual growth

**~$6B by 2028**

DELL'ORO GROUP

# Only FPGA SmartNICs Offer Both Performance & Flexibility

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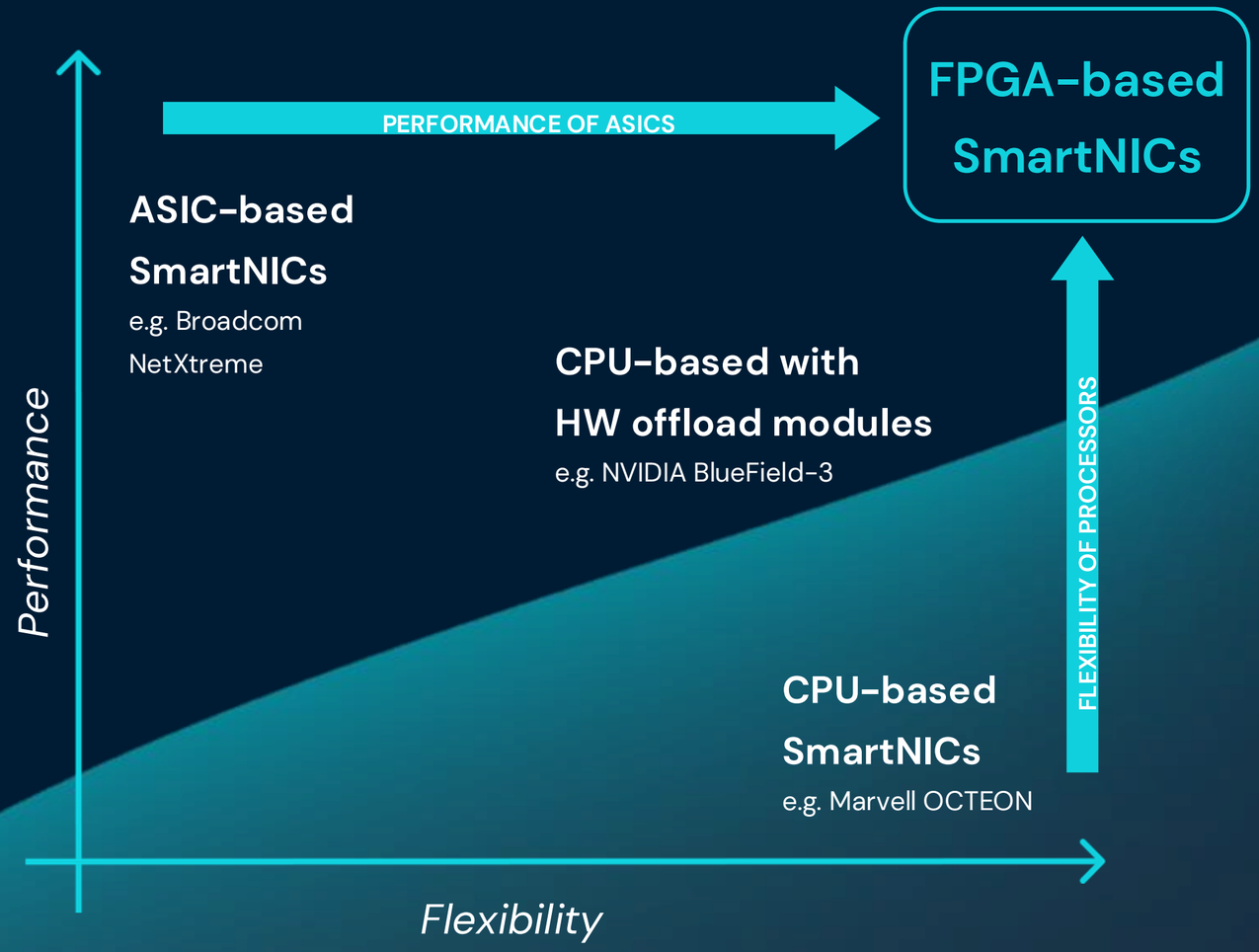


## FPGA-based SmartNICs

- High flexibility
- High performance

But FPGA design expertise required

**Performance + Flexibility + Time to Market + Vendor Neutrality → unreachable without DYNANIC.**

# DYNANIC Makes Deployment of FPGA SmartNICs Instant

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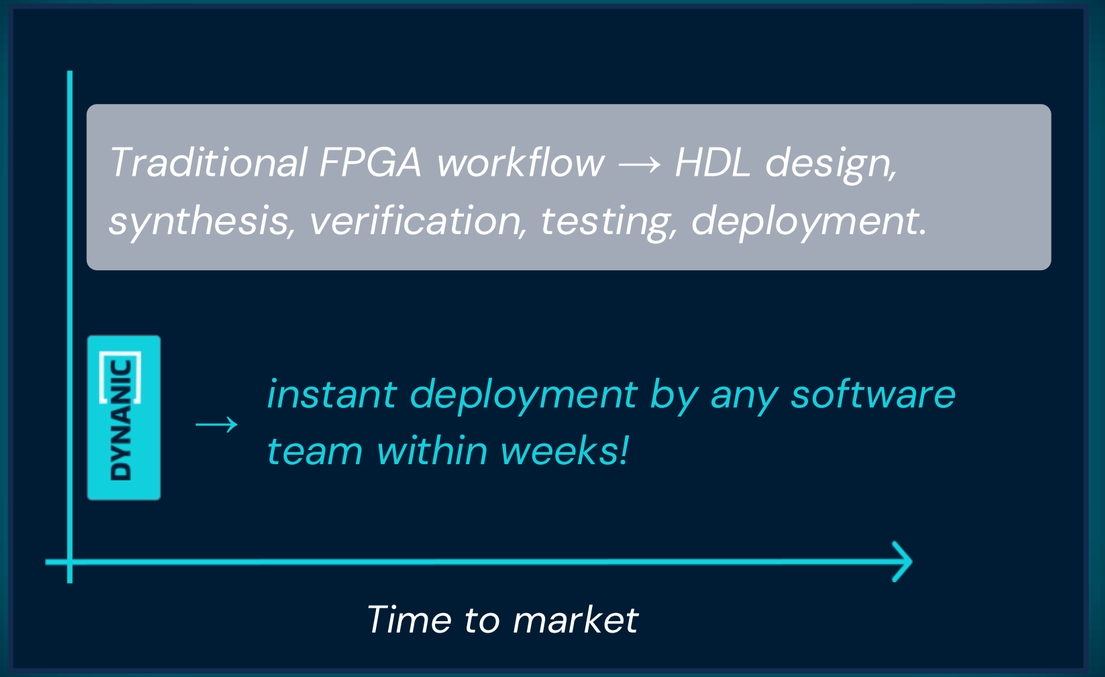
## FPGA-based SmartNICs are locked behind complexity of FPGA design

- 6 - 12 months to develop FPGA functionality
- Requires HDL engineers and proprietary toolchains
- Long verification, validation and release cycles
- It's often cheaper to wait for a new ASIC




DYNANIC

**Unlocks the full potential of any FPGA SmartNIC — no HDL required.**

*Prebuilt FPGA profiles and full software control enable instant deployment.*

*Vendor-neutral, proven across major platforms, backed by 20+ years of R&D.*

# DYNANIC: Turns Any FPGA Card into Fully-fledged SmartNIC Solution

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## DYNANIC Software

- The only **400 Gbps ready solution** for FPGA
- Extreme **flexibility thanks to** FPGA
- No-code on FPGA level => **time to market**
- FPGA chip and card vendor **independent**

### End applications utilizing software API

- **Networking**
  - Flow monitoring
  - Packet processing
  - Load balancing ...
- **Security**
  - Firewall
  - AntiDDoS
  - DPI ...
- **Storage**
  - NVMe-oF
  - RDMA
  - Compression ...
- Other applications

CPU

### Host system with software API

### FPGA packet processing pipeline

Networking profile | Security profile | Storage profile | ----- | Other profiles

FPGA

### Hardware abstraction layer

CARD

### Available on all major manufacturers

AMD XILINX, BittWare a molex company, prodesign, reflex ces, Silicom Denmark FPGA SOLUTIONS, iWave Global, napa:tech; RECONFIGURABLE COMPUTING, terasIC www.terasic.com

# Unlocking Value Across Key Application Domains

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## High-performance and flexible acceleration for modern network workloads

### Cybersecurity

**High-speed inline detection and protection**

- 400G+ packet filtering, DDoS mitigation, and DPI
- Enables deep network data analysis and feature extraction
- Instantly adaptable to new attack patterns

**Extreme performance with real-time flexibility**

### Storage & Data

**Accelerated data movement and security**

- NVMe-oF and Ultra Ethernet
- Encryption and compression at HW level on SmartNIC
- Up to 5× lower storage latency
- Easily reconfigurable for new workloads and standards

**Secure, fast and future-proof data infrastructure**

### Cloud & Edge

**Scalable offload for next-gen services**

- Flow steering, load balancing, and telemetry at terabit scale
- Up to 80 % CPU savings via SmartNIC acceleration
- Configurable pipelines for AI and 5G workloads

**Maximum efficiency and adaptability at scale**

DYNANIC

**One FPGA platform – multiple high-performance applications.**

*DYNANIC delivers performance and adaptability where it matters most.*

# Proven & Scalable Business Model

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## Per card license fee

- **Final price of the solution: 1/2 card + 1/2 license**
  *100G card ~5k€, 400G card ~15k€, …*
- **Price increases with the network link speed**
  *100G, 400G, 800G, 1.6T, ….*
- **Revenue scaling**
  *with the number of licensed cards*
- **Volume discounts**
  *to support large scale deployments*

## Per customer revenue

**based on number of installations**

**Small App & Solution Providers**
€100k - €200k

**Large App & Solution Providers**
€1M - €2M

**Hyperscalers**
€10M – €25M

**This model is widely used and proven by partners and customers:**

kaloom networks
kakaoenterprise
Silicom Ltd. Connectivity Solutions
AMD
altera An Intel Company
TPAC UNIQUE IN NDT

# Capital-Efficient Scale Through Ecosystem Partnerships

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## We Scale Through FPGA Chip & Card Vendors

- We turn their hardware into deployable SmartNIC solutions
- They introduce DYNANIC into enterprise and telco accounts
- Joint go-to-market & co-selling

FPGA Chip Vendors

altera AMD Achronix Data Acceleration

Card Vendors

Silicom Ltd. Connectivity Solutions napa:tech; terasIC

Phase 1
### Cybersecurity

- Anti-DDoS, IDS/IPS, DPI
- Inline 100–400G filtering
- Urgent demand

Phase 2
### Telco & Data Centers

- Network acceleration
- Larger licensed deployments
- Low latency infrastructure

Phase 3
### *Mass Deployment*

- Per-card licensing scale
- Multi-segment expansion
- High-volume licensing

**Focused entry → expanding deployments → hyperscale licensing.**

# Accelerating Towards Market Leadership: A Clear Path to Scale

## 2025—2026

**3+ customers**
**>10x revenue growth**
**15+ FTEs**

Building **ecosystem of partners** and **sales and marketing teams.** Integration of **new 400G cards** and **new features**.

## 2027—2028

**10+ customers**
**>10x revenue growth**
**25+ FTEs**

**Scaling up** sales & marketing in US to acquire new and convert large customers (phase1 -> phase2). Expansion of **application profiles.**

## 2029—2031

**50+ customers**
**>14x revenue growth**
**100+ FTEs**

Transition to **volume licensing** across deployed infrastructure. **Expansion within existing large customers** Standardization of SmartNIC deployments

# Summary & Call to Action

DYNANIC

- Booming market
- Unique technology
- Serial founding expertise

**We are building the future of network acceleration.**
**DYNANIC is removing the barrier to using FPGAs.**
**Join DYNANIC and power this revolution!**

## Seeking for strategic investment that enable:

— **Accelerated Market Expansion:** Rapidly scale our sales and marketing teams to directly penetrate key accounts.

— **Core Product Evolution:** Integrate advanced features for critical security and network infrastructure use cases, expanding our features and capabilities.

— **Deepening Partnerships:** Forge stronger strategic alliances with application and solution providers.

— **Unlocking Growth:** Significantly accelerate our market penetration and solidify our position as “the ARM of SmartNICs”.

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