

Complete FPGA-based SmartNIC solution

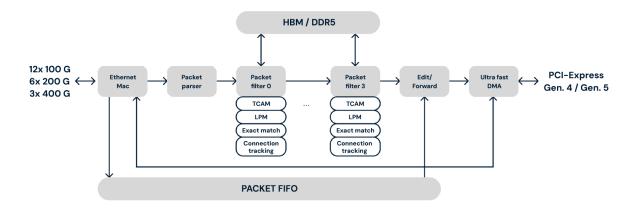
on FALCON Agilex® 7 M Acceleration Card from



SmartNIC (Smart Network Interface Card – NIC) is a specialized network adapter that offloads certain processing tasks from the host CPU to the NIC itself. These tasks can include packet filtering, encryption/decryption, compression, and other functions that are typically performed by software running on the host. By moving these tasks to the SmartNIC, it reduces the load on the host CPU, increases network performance and significantly improves overall efficiency. The FPGA allows for flexibility in terms of the specific tasks that can be performed, as well as the ability to easily update or modify the SmartNIC's functionality as needed.

How DYNANIC makes complete solution on FALCON card?

Programming FPGA is not an easy task. DYNANIC comes with the universal high-speed FPGA packet processing pipeline for FALCON. This pipeline consists of components required for various packet processing in many use-cases. And so DYNANIC enables full utilization of FPGA-technology without prior FPGA knowledge!



This wire-speed capable FPGA pipeline is **controlled from the host software** by standardized and open-source RTE Flow DPDK API. For example, to set up the filtration rule in the pipeline, it is only needed to write **a few lines of code in** *C++* **or** *Python* programming language. So simple!



Use-cases with DYNANIC

Network acceleration

Examples of processing tasks that can be offloaded with DYNANIC include packet processing, encryption/decryption, compression/decompression or implementation and acceleration of future network protocols.

Network security

Example applications are firewall, intrusion detection and prevention systems (IDS/IPS), and DDoS mitigation (Anti-DDoS). All these can be offloaded with DYNANIC for improving performance and reducing latency.

Network monitoring and analyses

Troubleshooting network issues, detecting anomalies, and identifying performance bottlenecks with full wire-speed traffic capture in real time is possible thanks to DYNANIC.

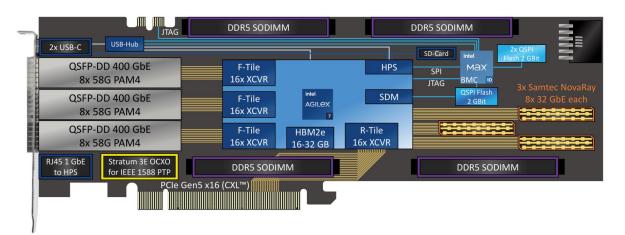
Content delivery

DYNANIC can help applications for accelerating content caching and delivery, improving user experience and reducing server load in content delivery networks (CDNs).

Virtualized networking

Virtualized networking functions (NFV) such as virtual switches and routers improve performance and reduce latency using DYNANIC solution.

And many more . . .



New FALCON Agilex® 7 M Acceleration Card by PRO DESIGN Electronic GmbH.

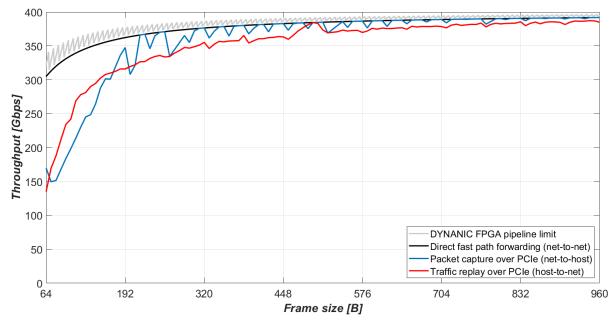


Unique features of DYNANIC solution on FALCON

- No need for FPGA development, processing pipeline is given
- Standardized and open-source DPDK software stack
- Processing pipeline controlled by standard RTE Flow interface
- ☐ Full 400 Gbps throughput to and from host RAM
- □ Configurable packet parser supporting protocols from L2 to L4
- Different filtering options utilizing internal or external memories
- Flow tracking with fast atomic insert/removal from host
- Solution ready for different link speeds even on given card
- □ **Lossless traffic** processing at wire-speeds from 100G to 400G

Link speed configuration	Exact match	LPM prefixes	TCAM rules
100 Gbps	2.5 M	1.5 M	15 k
200 Gbps	2.5 M	1.5 M	8 k
400 Gbps	1 M	0.7 M	4 k

Achievable rule capacity for different filter types on **FALCON**. Values provided for IPv4 address matching. Exact match and LPM can be combined by sharing the capacity.



DYNANIC throughput on 400GbE and PCIe gen5 hardware cards. On-chip pipeline is calibrated for sustained wire-speed processing with a small margin on top. The only performance bottlenecks are introduced by the overhead of DPDK transfers over PCIe.



BRNO : L O G I C

BrnoLogic offers custom design and development services for FPGA-based projects. For more than 20 years are company team members specializing in the acceleration of algorithms required for high-speed network packet processing (e.g. packet parsing, packet/headers fields extraction, hash based pattern matching, filtering, traffic flow management, etc.) with link speeds up to 400 Gbps. Unique portfolio of IPs was also utilized to bring FPGA technology closer to any software company. That's how **DYNANIC** solution was created.

prodesign

Based on more than 30 years of practice in the E²MS market we have extensive knowledge in the areas of electrical engineering, PCB design, mechatronics, production engineering, process engineering, measuring and testing – decades of experience that you can benefit from.

Our specialists work closely together and take advantage of interdisciplinary synergies. The results are powerful, cost-efficient high-end solutions characterized by sophisticated concepts, short process cycles and high reliability.

How to start?

Contact prodesign to obtain FALCON card at

sales-fpga-acceleration@prodesign-europe.com

and visit

https://dyna-nic.com to download working
DYNANIC package for FALCON cards